



Benha University

Logic Circuits ECE 224C

Computer Systems Engineering
Electrical Engineering Department



Faculty of Engineering
(at Shoubra)

Sheet 2

Solve the following problems from *Digital Design: With an Introduction to the Verilog HDL*:

- 1.14
- 1.17
- 1.18
- 1.19
- 1.23
- 1.24
- 1.25
- 1.27
- 1.32
- 1.33